

[illegible]

APPLICATION NO. 09/910053	CONT/PRIOR	CLASS 36 396	SUBCLASS 542	ART UNIT 2841 2851	EXAMINER Blackman Maaney
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Chih-Yu Ting

Structure of a chip package

PTO-2040
12/89

<p>_____</p> <p>_____</p> <p>_____</p>	<p>_____</p> <p>_____</p> <p>_____</p>	<p>Amount Due</p>	<p>_____</p>
<p>_____</p>	<p>_____ (Date)</p>	<p>_____</p>	<p>_____</p>
<p><input checked="" type="checkbox"/> The period _____ months of this patent have been disclaimed.</p>	<p>_____ (Legal Instruments Examiner)</p>	<p>_____ (Date)</p>	<p>_____</p>

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